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08/26/2004

J C PATENTS INC
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Suite 250
Irvine, CA 92618

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01 FC:1501 1370.00 DA
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JIAWEI HUANG	(Depositor's name)
	(Signature)
NOVEMBER 16, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/853,005	05/09/2001	Chau-Chad Tsai	JCLA5312	5161

TITLE OF INVENTION: PERIPHERAL DEVICE INTERFACE CHIP CACHE AND DATA SYNCHRONIZATION METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330 1370	\$300	\$1630 1670	11/26/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
BAKER, PAUL A	2188	711-141000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

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VIA TECHNOLOGIES, INC.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TAIPEI HSIEN, TAIWAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date 11/16/2004

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